

ABSTRACT OF THE DISCLOSURE

A video signal processing apparatus processes input video signals. A video signal source supplies the input video signals carrying at least a first video signal that is an interlaced signal having 480 effective scanning lines and a second video signal that is an interlaced signal having 1080 effective scanning lines. A video signal processor converts at least the first and the second video signals into a third video signal that is a progressive signal having 1440 effective scanning lines, thus outputting the third video signal. The output third video signal may be converted into a fourth video signal that is an interlaced signal by a progressive-to-interlace converter, thus outputting the fourth video signal.

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